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17. Th	e power processing	device according	to claim 16,	further	including a	first s	shielding
layer disposed	on an exterior surfac	ce of said multilay	yer printed c	ircuit bo	ard.		

- 18. The power processing device according to claim 16, further including an electrically conductive trace contained between two layers of said dielectric sheets, said conductive trace communicating with said first set of electrically conductive windings.
- 19. The power processing device according to claim 18, further including at least one component secured to an exterior surface of said multilayer printed circuit board at a location over said electrically conductive trace.

REMARKS

By this preliminary amendment, claims 16-19 have been added. No new matter is involved as the original specification, drawings, and claims fully support these claims.

Respectfully Submitted,

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Date:	

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